

**RESPONSE UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE  
GROUP ART UNIT 2182**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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Application No.: 10/690,266	§	Examiner:	Phan, Dean
Filed: May 20, 2004	§	Group/Art Unit:	2182
Inventor(s):	§	Atty. Dkt. No:	6057-61200
Dominik J. Schmidt	§	Conf. No.	4584
	§		
	§		
	§		
Title: INTEGRATED CIRCUIT	§		
WITH A PLURALITY OF	§		
HOST PROCESSOR	§		
FAMILY TYPES	§		
	§		
	§		
	§		

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**AMENDMENT; RESPONSE TO FINAL OFFICE ACTION OF  
OCTOBER 30, 2008**

**MAIL STOP AF**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Final Office Action of October 30, 2008, to further highlight why the application is in condition for allowance.